FIG. 1

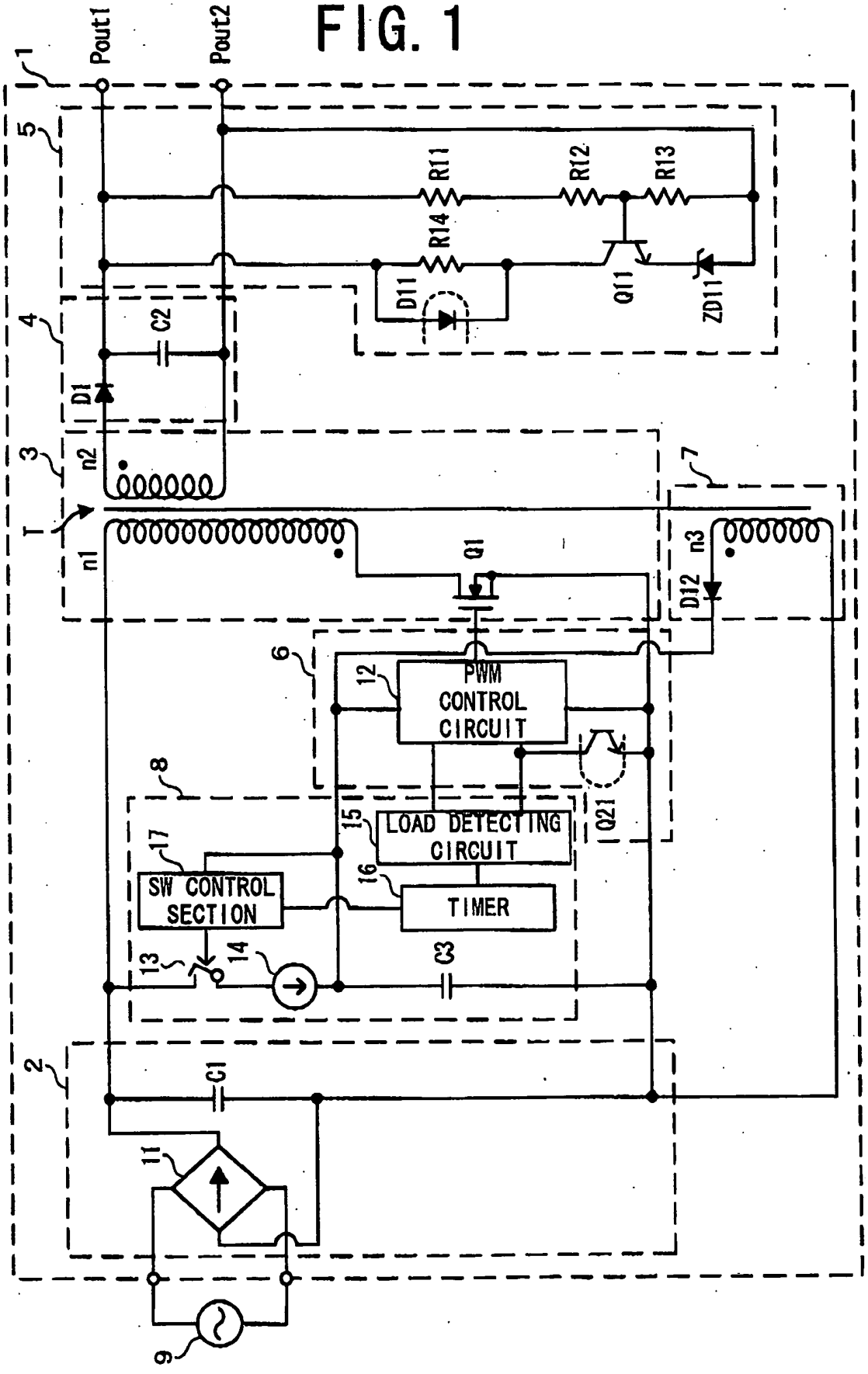
Pout1
Pout2
1
5
R11
R12
R13
R14
D11
Q11
ZD11
4
C2
D1
3
n2
7
n1
Q1
n3
D12
6
12
PWM CONTROL CIRCUIT
8
Q21
15
LOAD DETECTING CIRCUIT
17
SW CONTROL SECTION
16
TIMER
13
14
C3
2
C1
11
9

FIG. 2

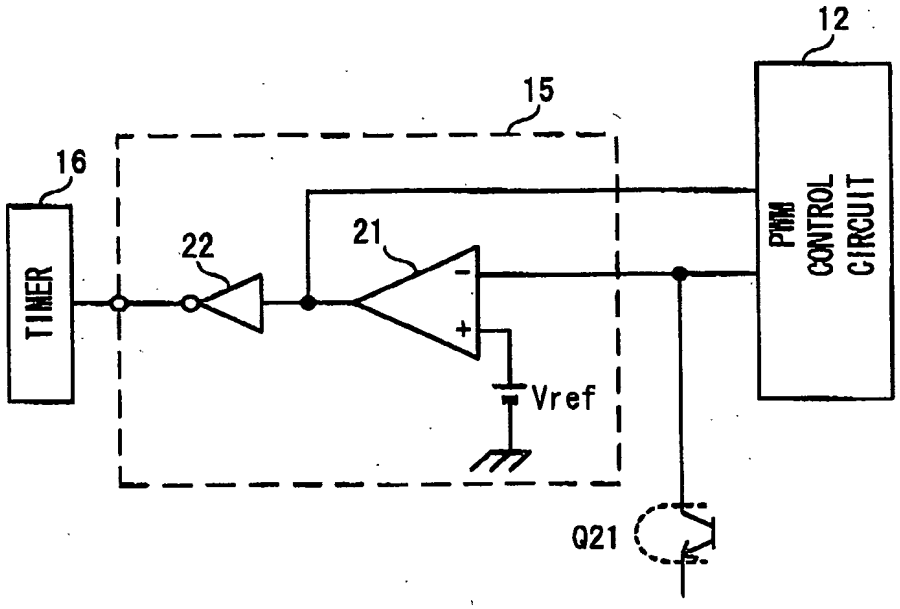

FIG. 3

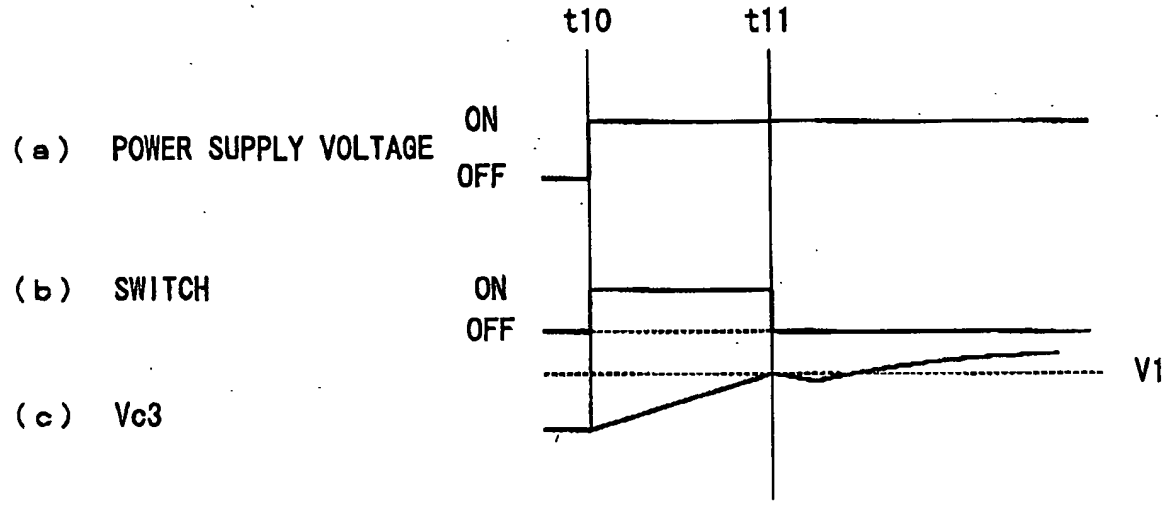

FIG. 4

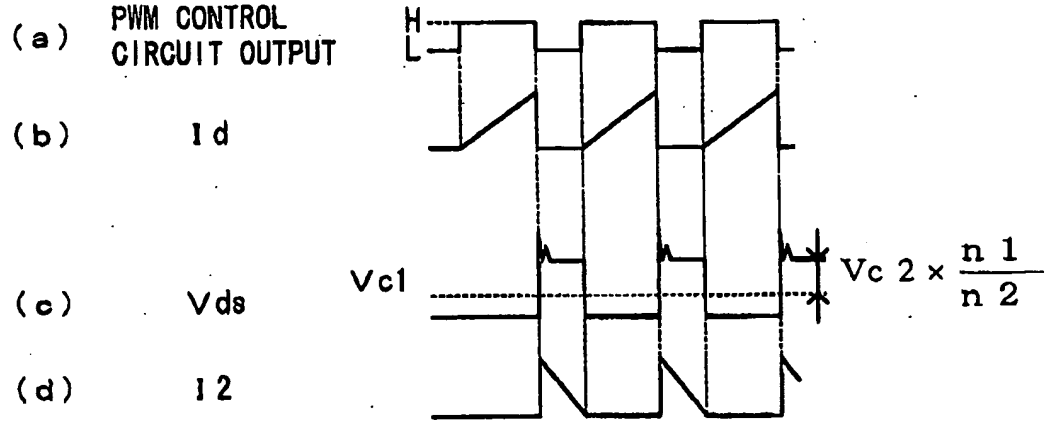

FIG. 5

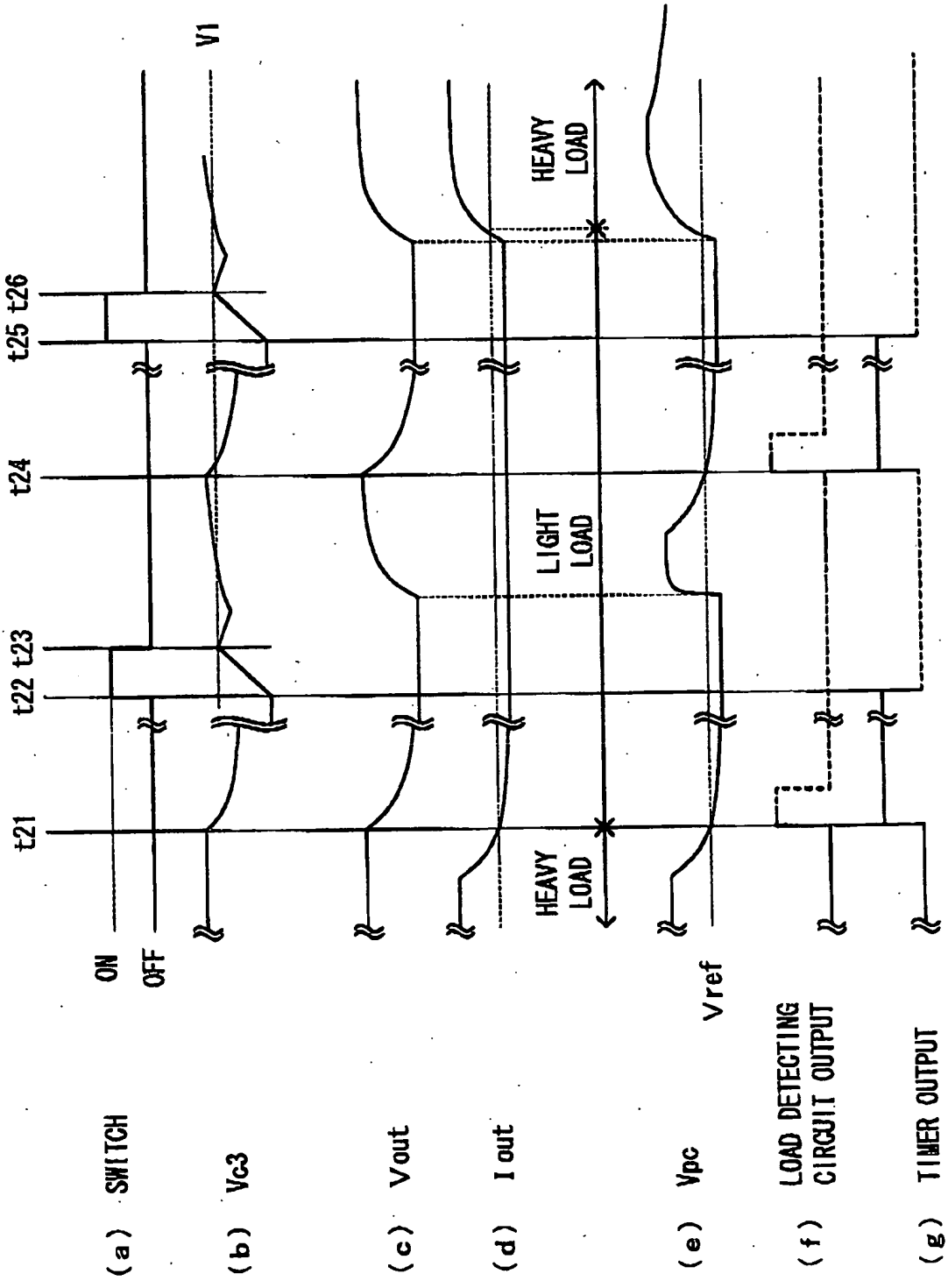

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FIG. 6

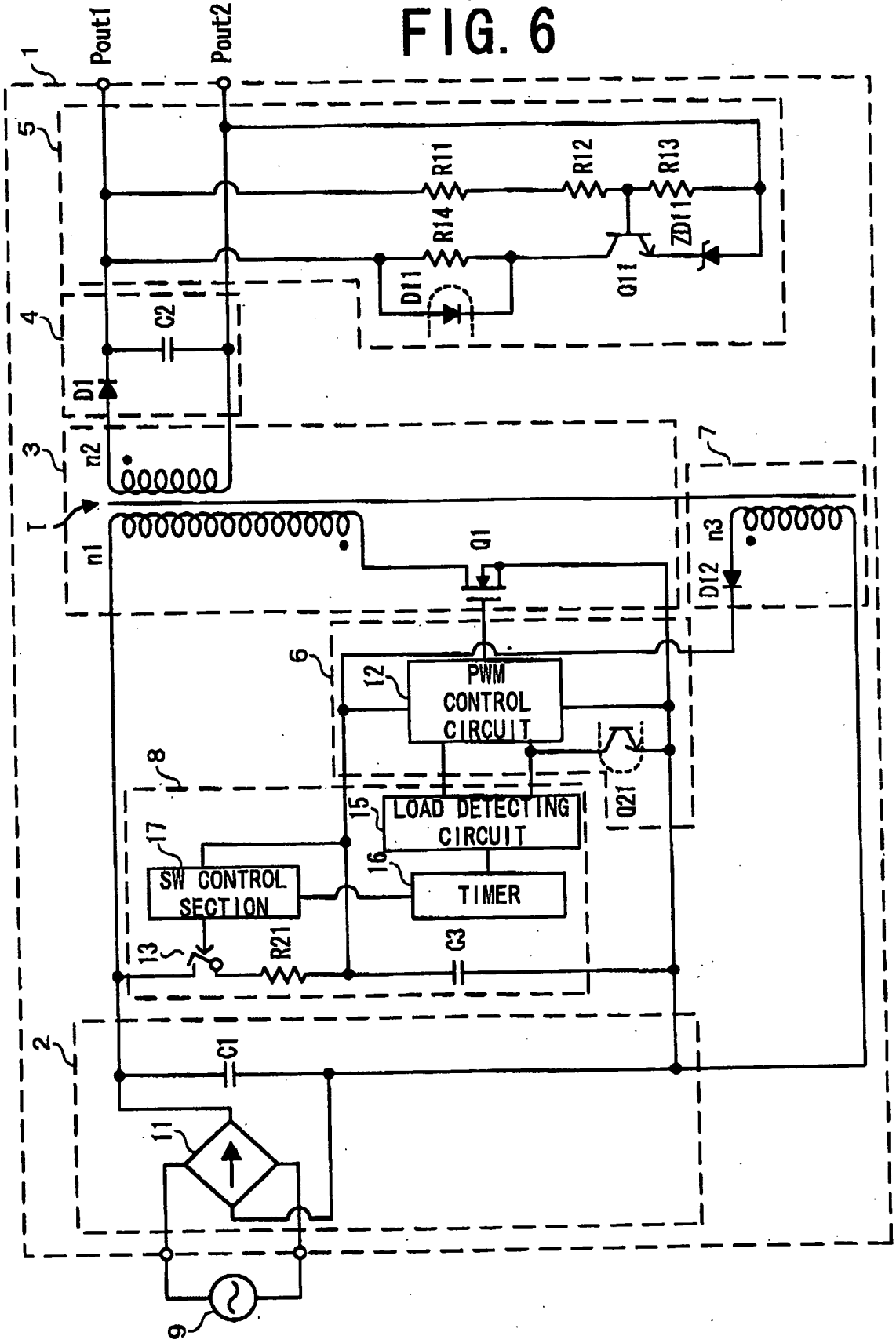
Pout1
Pout2
1
5
R11
R12
R13
R14
ZD11
Q11
D11
4
C2
D1
3
n2
7
I
n1
Q1
n3
D12
6
12
PWM CONTROL CIRCUIT
8
15
LOAD DETECTING CIRCUIT
Q21
17
16
SW CONTROL SECTION
TIMER
13
R21
C3
2
C1
11
9

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FIG. 7

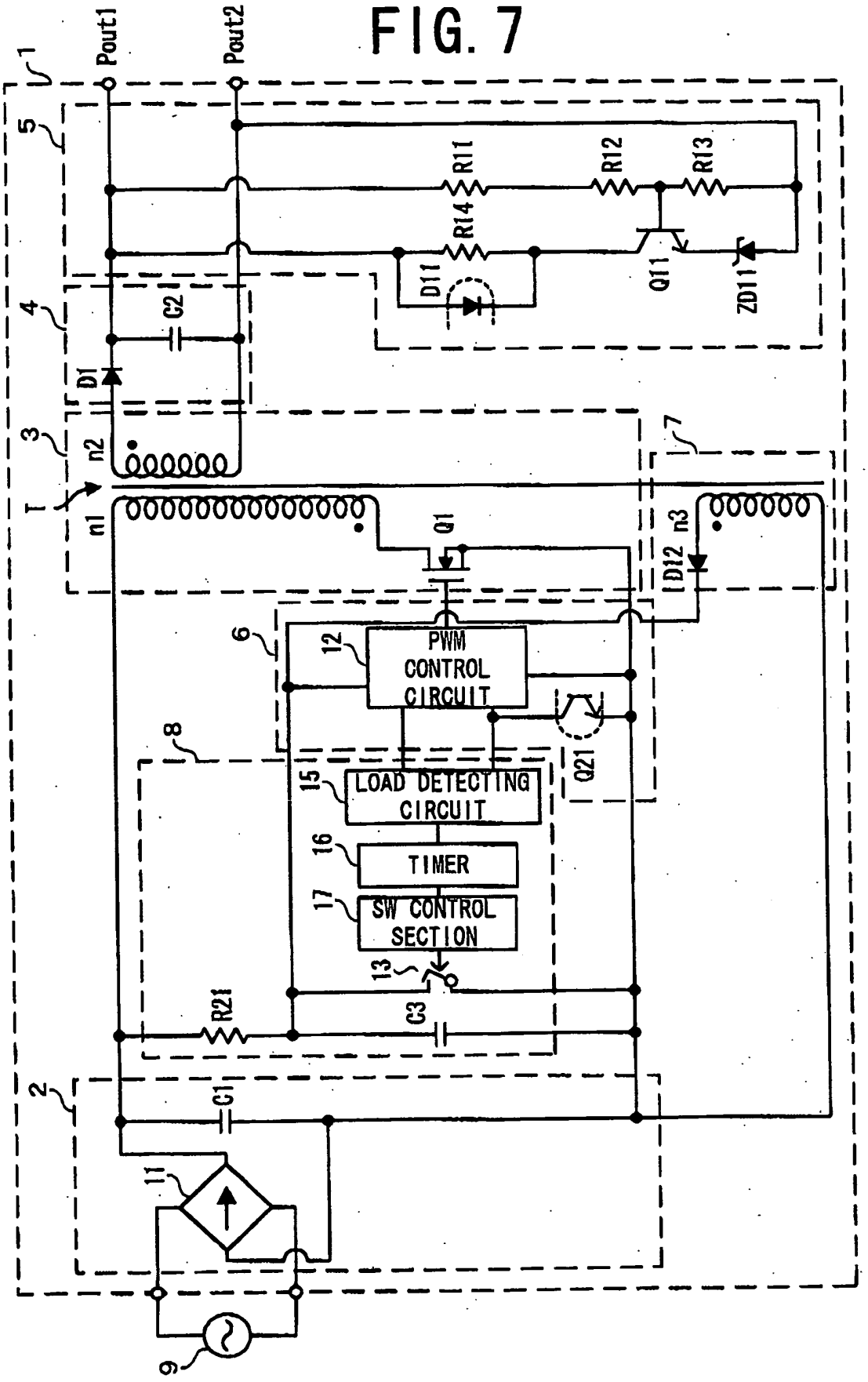

Pout1
Pout2
1
5
R11
R12
R13
R14
D11
Q11
ZD11
4
C2
D1
3
n2
T
n1
7
Q1
n3
D12
6
12
PWM CONTROL CIRCUIT
8
15
LOAD DETECTING CIRCUIT
16
TIMER
17
SW CONTROL SECTION
13
Q21
R21
C3
2
C1
11
9

FIG. 8

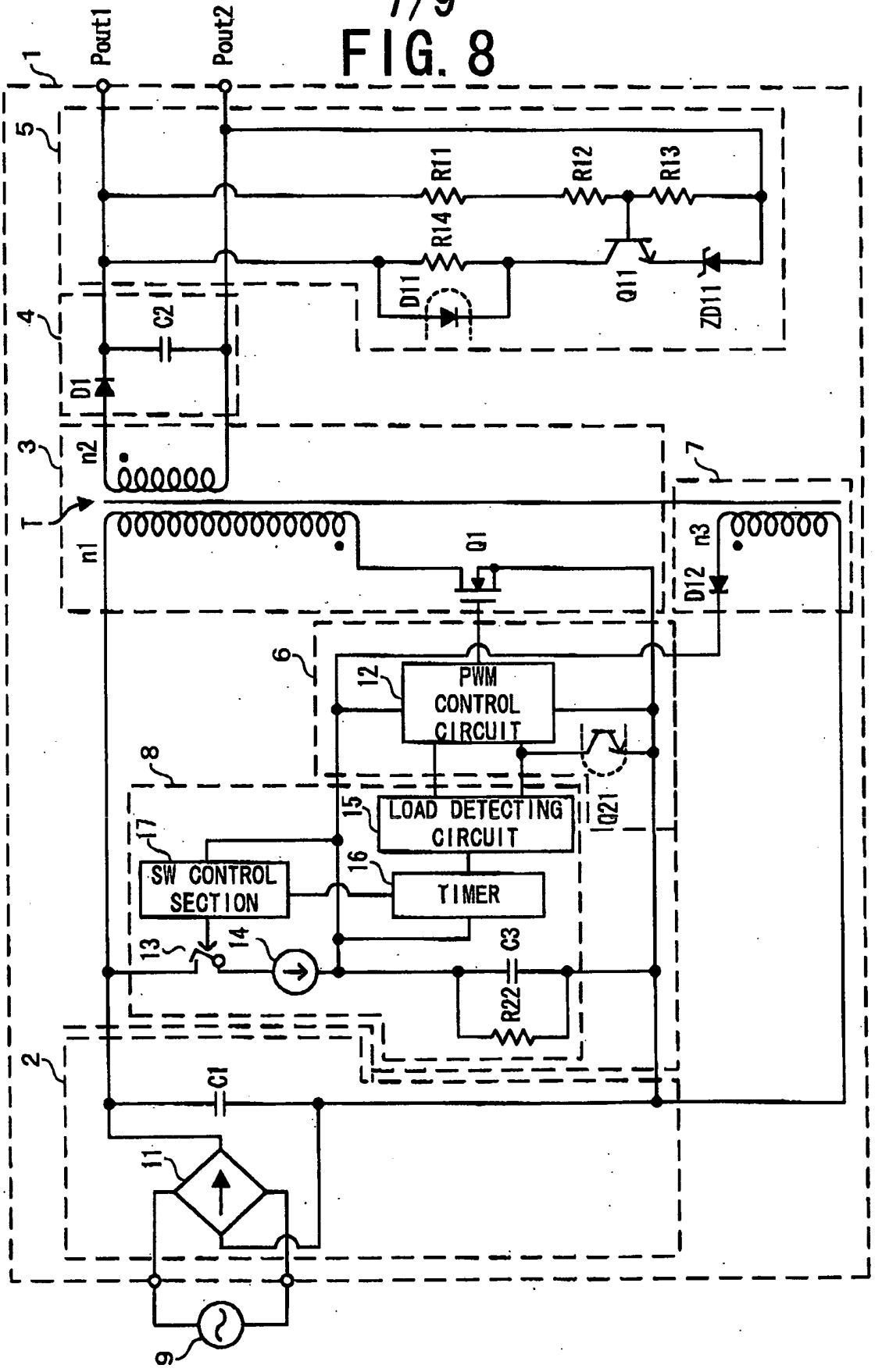

Pout1
Pout2
1
5
R11
R12
R13
R14
D11
Q11
ZD11
4
C2
D1
3
n2
T
n1
7
Q1
n3
D12
6
12
PWM CONTROL CIRCUIT
8
15
LOAD DETECTING CIRCUIT
Q21
17
SW CONTROL SECTION
16
TIMER
13
14
C3
R22
2
C1
11
9

FIG. 9

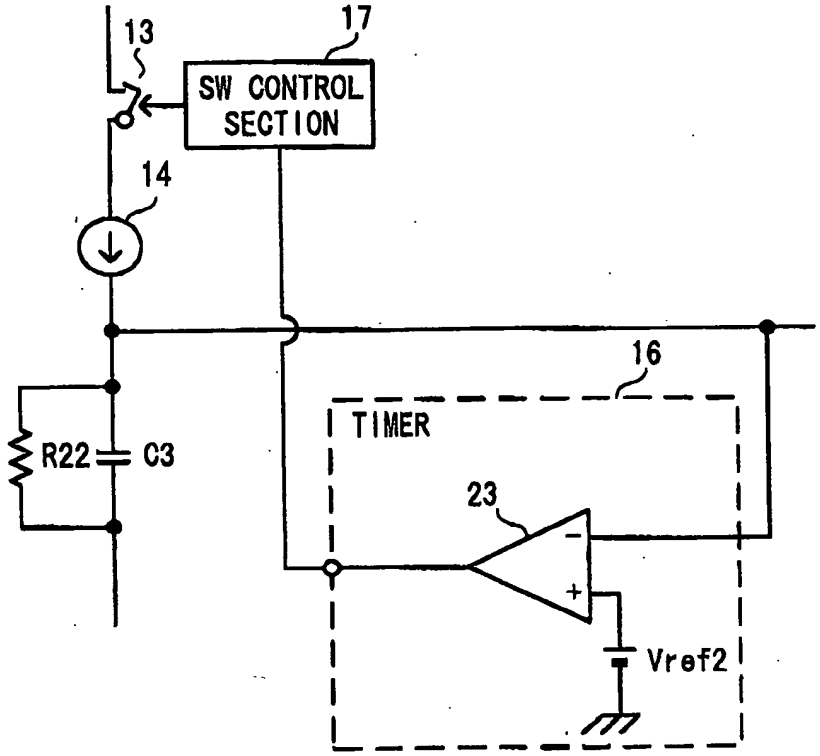


FIG. 10

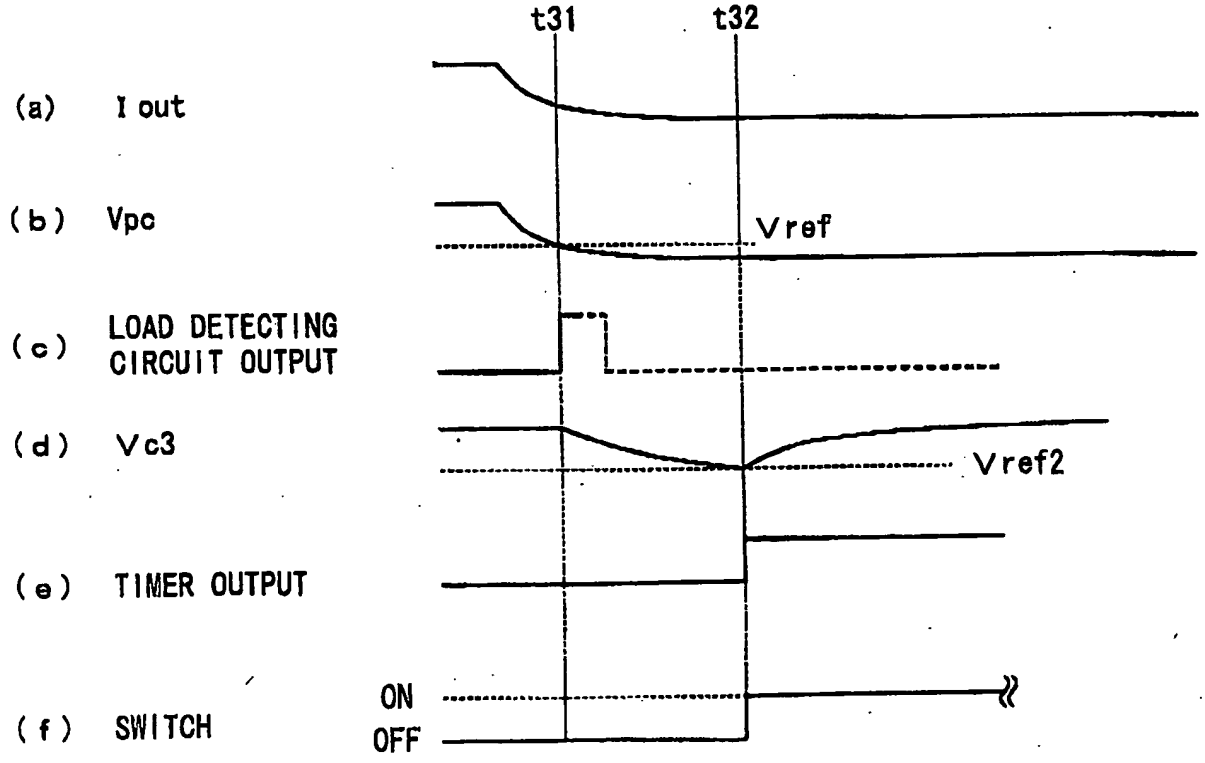
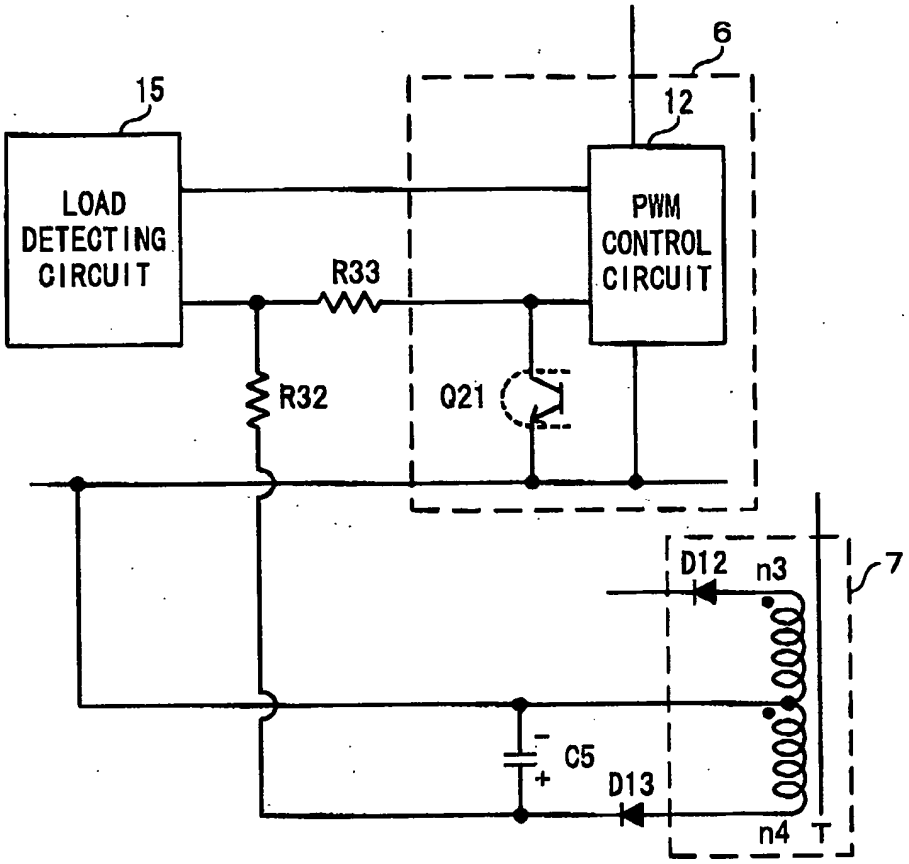


FIG. 11


6
15
12
LOAD DETECTING CIRCUIT
PWM CONTROL CIRCUIT
R33
R32
Q21
D12
n3
7
C5
D13
n4
T